

**IN THE SPECIFICATION**

Please amend paragraph 0078 of the specification as follows:

[0078] It should also be noted that embodiments of the present invention may be implemented and not only as a physical circuit or module (e.g., on a semiconductor chip) but, also within a machine-readable media. For example, the circuits and designs described above may be stored upon, or embedded within, a machine-readable media associated with a design tool used for designing semiconductor devices. Examples include a netlist formatted in the VHSIC Hardware Description Language (VHDL), the Verilog language, or the SPICE language. Some netlist examples include a behavioral level netlist, a register transfer level, (RTL) netlist, a gate level netlist, and a transistor level netlist. Machine-readable media include media having layout information, such as a GDS-II file. Furthermore, netlist files and other machine-readable media for semiconductor chip design may be used in a simulation environment to perform any one or more methods described above. Thus it is also to be understood that embodiments of the present invention may be used, or to support, a software program executing on some processing core (e.g., a CPU of a computer system), or otherwise implemented or realized within a machine-readable medium. A machine-readable medium may include any mechanism for storing and transmitting information in a form readable by a machine (e.g., a computer). For example, a machine readable-readable medium may comprise a read-only memory (ROM), a random access memory (RAM), magnetic disc storage media, optical storage media, and flash memory devices, ~~electrical, optical, acoustic, or other~~ form of propagated signal (e.g., ~~a carrier wave, infrared signal, radio-frequency signal, a digital signal, etc.~~).